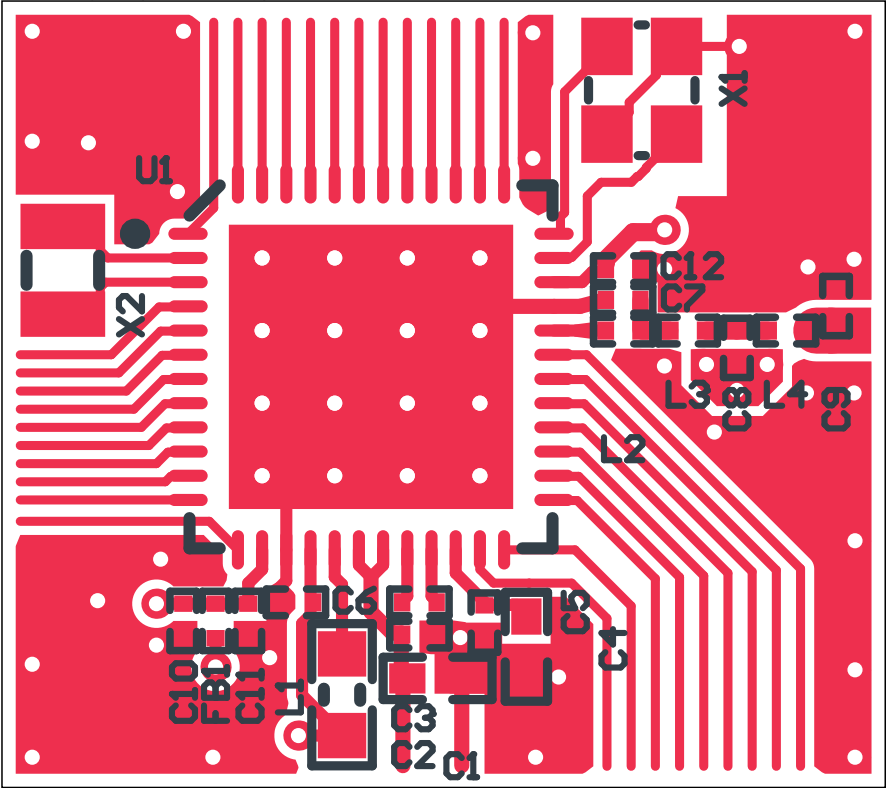


Top Layer (Scale 8:1)



Layout notes:

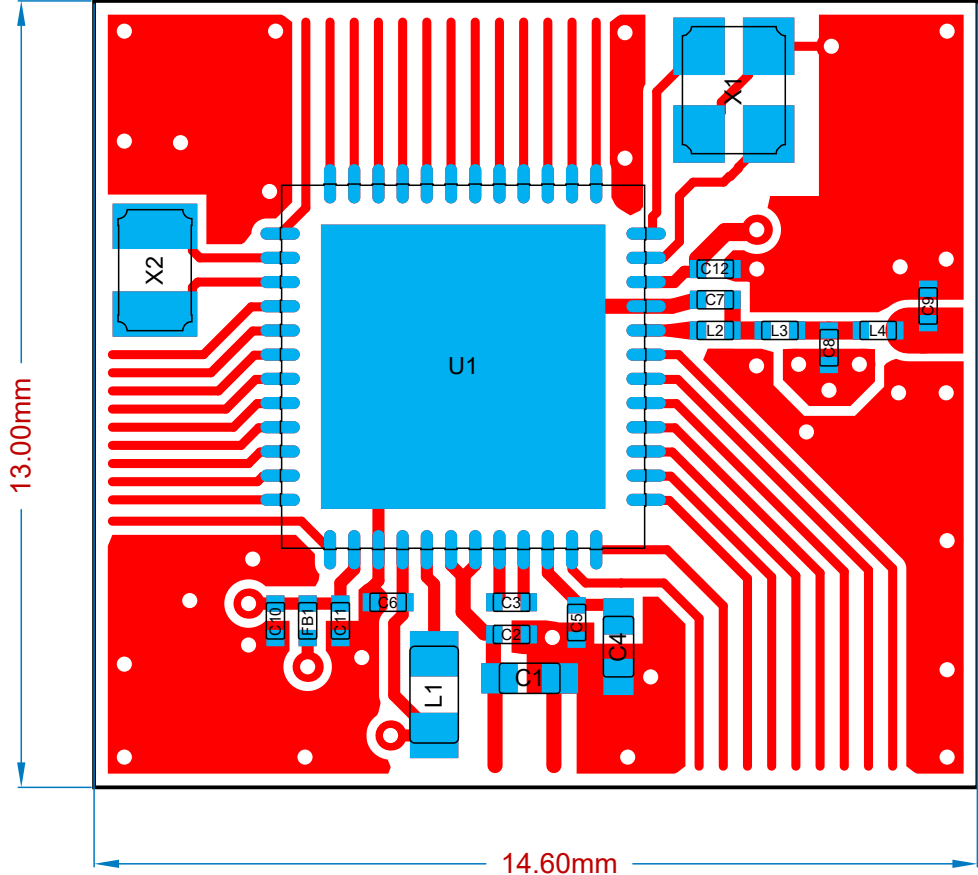
- Grounding of C7 capacitor must ONLY be connected to Pin33 (VSS\_PA) in top layer.
- Pin33 must ONLY be connected to Pin49 (VSS center pad) underneath the IC package
- Grounding of C8 capacitor MUST be isolated from ALL ground layers except the bottom ground layer.
- Prepreg thickness between TOP and Mid-Layer 1 shall be  $\leq 150\ \mu\text{m}$

Nordic Semiconductor does not warrant the accuracy or completeness of this documentation or any included information. Nordic Semiconductor makes no assurances that this design will meet specification, be appropriate for your application, or fit for any particular purpose, or that it will function in an implementation. Nordic Semiconductor also does not claim that the design is ready for production. It is your responsibility to thoroughly validate and test your design implementation to confirm the functionality of your application.  
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Title VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-QFAA	Revision: 0.3
Date: 20.11.2025			Sheet 1 of 9
File: nRF54LV10A_qfaa_4L_config1.PCBdWf			Drawn By: DABA
Classification: Public			



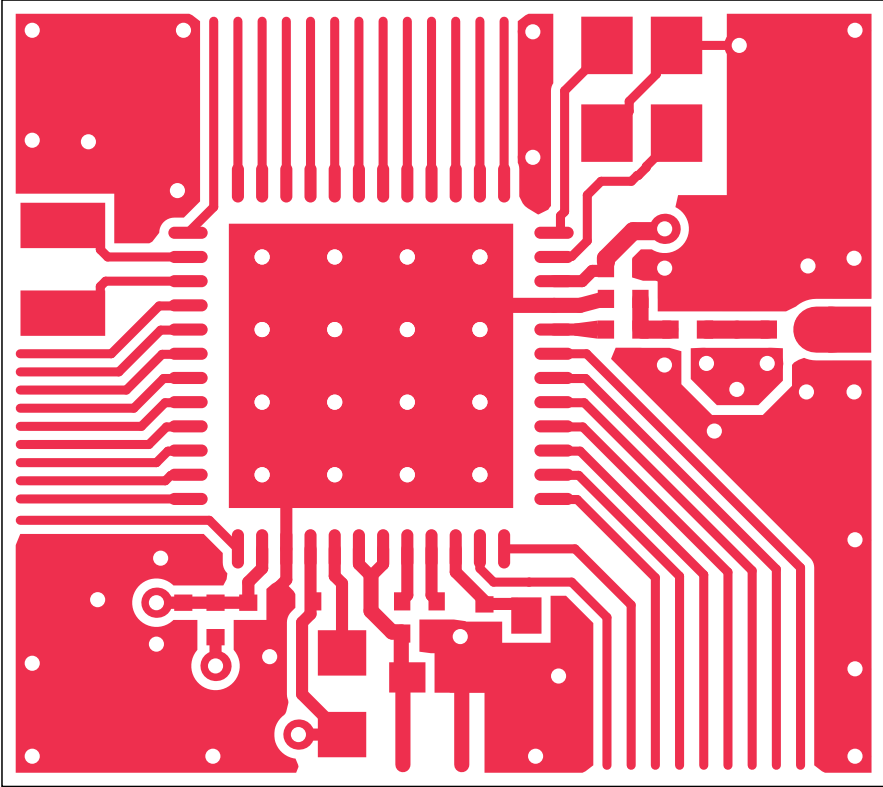
View from Top side (Scale 8:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-QFAA	Revision: 0.3
Date: 20.11.2025			Sheet 2 of 9
File: nRF54LV10A_qfaa_4L_config1.PCBDwf			Drawn By: DABA
Classification: Public			



Top Layer (Scale 8:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-QFAA	Revision: 0.3
Date: 20.11.2025			Sheet 3 of 9
File: nRF54LV10A_qfaa_4L_config1.PCBDwf			Drawn By: DABA
Classification: Public			



A

B

C

D

E

F

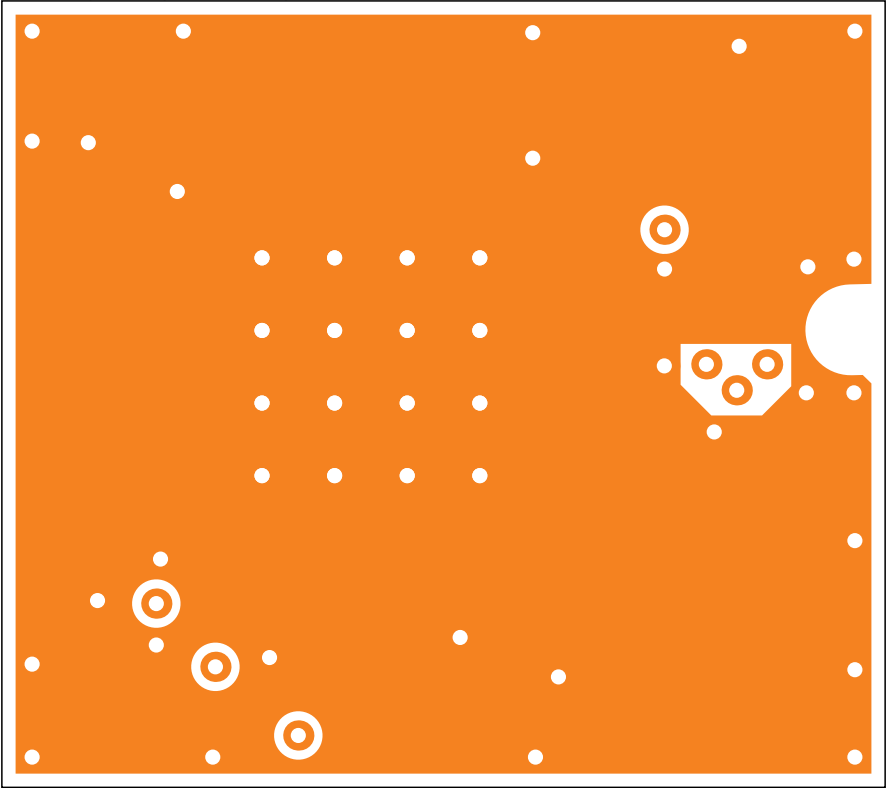
1

2

3

4

Mid-Layer 1 (Scale 8:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	1
Date:	20.11.2025	Device:	nRF54LV10A-QFAA
File:	nRF54LV10A_qfaa_4L_config1.PCBDwf		Revision:
Classification:	Public		Sheet
		Drawn By:	DABA
		of	
		9	



A

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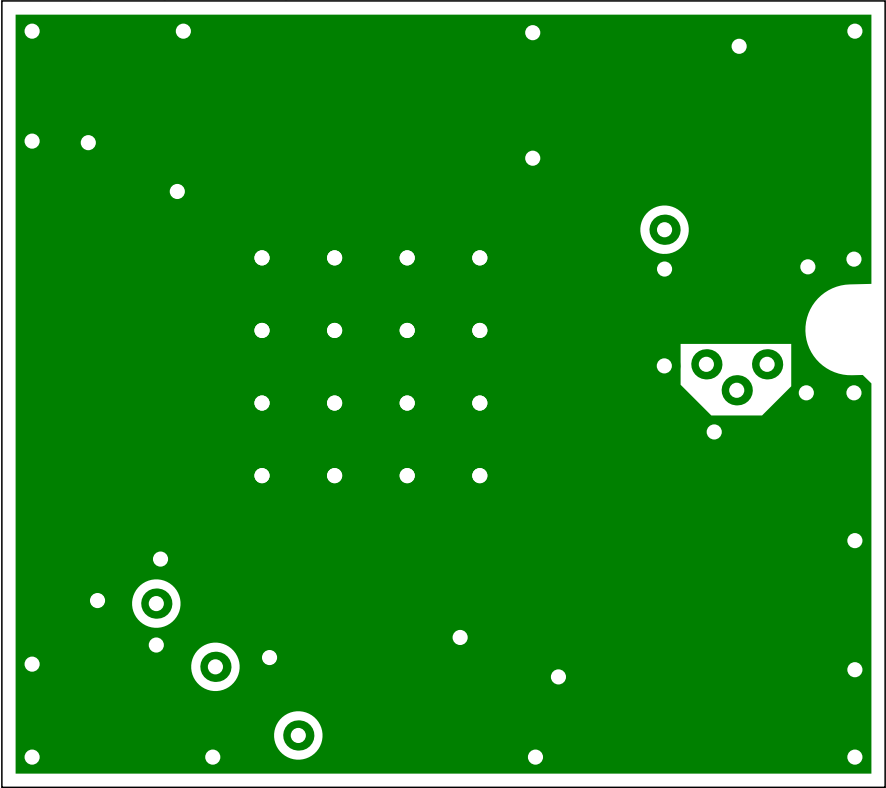
1

2

3

4

Mid-Layer 2 (Scale 8:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-QFAA	Revision: 0.3
Date: 20.11.2025			Sheet 5 of 9
File: nRF54LV10A_qfaa_4L_config1.PCBDwf			Drawn By: DABA
Classification: Public			



A

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A

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E

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1

2

2

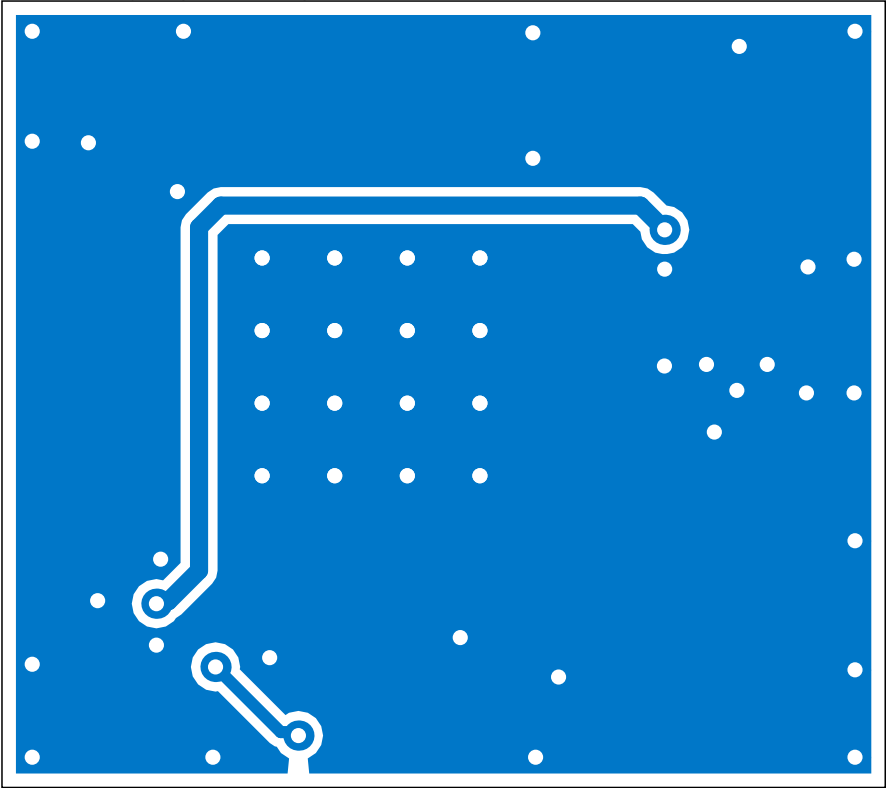
3

3

4

4

Bottom Layer (Scale 8:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-QFAA	Revision: 0.3
Date: 20.11.2025			Sheet 6 of 9
File: nRF54LV10A_qfaa_4L_config1.PCBDwf			Drawn By: DABA
Classification: Public			



A

B

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F

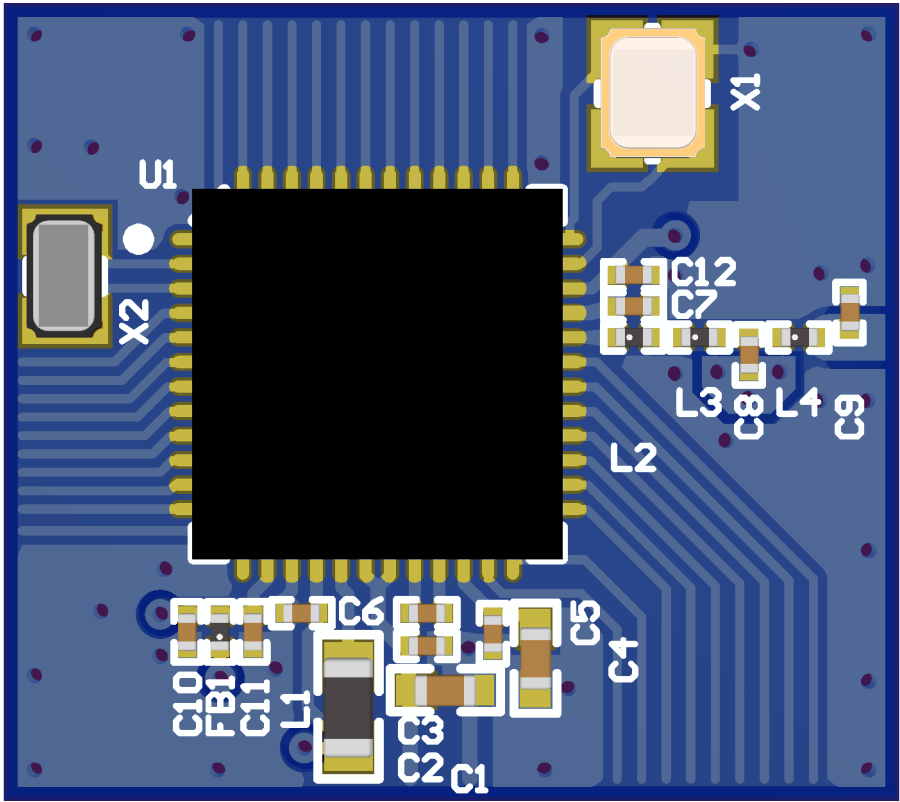
Layer Stack Legend

Material	Layer	Thickness	Type	Gerber
	Top Overlay		Legend	GTO
Surface Material	Top Solder	0.03mm	Solder Mask	GTS
Copper	Top Layer	0.04mm	Signal	GTL
Prepreg		0.10mm	Dielectric	
CF-004	Mid-Layer 1	0.04mm	Signal	G1
Core		1.30mm	Dielectric	
CF-004	Mid-Layer 2	0.04mm	Signal	G2
Prepreg		0.10mm	Dielectric	
Copper	Bottom Layer	0.04mm	Signal	GBL
Surface Material	Bottom Solder	0.03mm	Solder Mask	GBS
	Bottom Overlay		Legend	GBO
Total thickness: 1.69mm				

Title VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-QFAA	Revision: 0.3
Date: 20.11.2025			Sheet 7 of 9
File: nRF54LV10A_qfaa_4L_config1.PCBdWf			Drawn By: DABA
Classification: Public			



View from Top Side



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	1
Date:	20.11.2025	Device:	nRF54LV10A-QFAA
File:	nRF54LV10A_qfaa_4L_config1.PCBDwf		Revision:
Classification:	Public		Sheet
		Drawn By:	DABA
		8 of 9	





